IN THE DRAWINGS

Please amend the drawings as follows:

The reference of "FIG. 2" is changed to "FIG. 2A."

FIG. 2B is added.

AMENDMENT UNDER 37 C.F.R. §1.312 & COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE   **Page 5**
Serial Number: 10/643,742   Dkt: 1376.697US1
Filing Date: August 18, 2003
Title: DECOUPLING OF WRITE ADDRESS FROM ITS ASSOCIATED WRITE DATA IN A STORE TO A SHARED MEMORY IN A MULTIPROCESSOR SYSTEM

REMARKS

In the Notice of Allowance dated February 28, 2010, the Examiner stated at p. 2, *Examiner's Comment*, that the features of "enforcing memory ordering in subsequent load and store requests to the write request address until the write data associated with the write request is written into the shared memory" must be shown on the drawings or the features must be canceled from the claims.

The drawings and the relevant portions of the specification have been amended, as requested by the Examiner. The amendments are fully supported in the specification as originally filed, for example, at p. 5, line 19 through p. 8, line 6. Thus, no new matter has been added.

If necessary, please charge any additional fees or credit overpayment to Deposit Account 19-0743.

Respectfully submitted,

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Date   May 10, 2010   By   / Hong, myunggon /

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this ___10___ day of May 2010.

Jamie A. Ehlers   /Jamie A. Ehlers/

Name   Signature